



Problem 7. «A small present for you!»

Alice wants to implement the lightweight block cipher SMALL-PRESENT on a chip. She starts with the bit permutation that is illustrated in Figure below. For example, the input bit number 4 is moved to the output bit number 1, bit 6 is moved to 9 and so on. Clearly, many lines are intersecting, and this would cause a short circuit if the lines were metal wires. Is it possible to avoid this problem by using several “layers,” i.e., parallel planes? That is to draw the lines without intersections on each layer. We assume that

- the work area is a rectangle bounded by the lines where input and output bits are placed and the lines of the outermost connections $0 \rightarrow 0$ and $15 \rightarrow 15$;
- input and output bits are ordered; connections are represented by arbitrary curves;
- color of a line indicates the number of its layer, a line can change color several times;
- the point where a line changes color indicates a connection from one layer to another.

Q1 What is the minimum number of layers required for implementing in this way the SMALL-PRESENT bit permutation?

Q2 Find a systematic approach how to draw a valid solution for the minimum number of layers found in **Q1** and present the drawing!

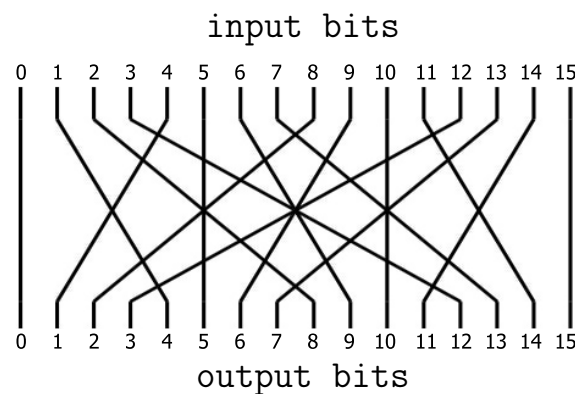


Figure: Illustration of the bit permutation used in SMALL-PRESENT